

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	New Application 10/668,969
				Filing Date	September 24, 2003
				First Named Inventor	Akira ISHIKAWA
				Art Unit	2818
Examiner Name	LE				
Sheet	1	of	1	Attorney Docket Number	740756-2653

U.S. PATENT DOCUMENTS					
Examiner Initials ¹	Cite No. ¹	U.S. Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
R		US 2002/0008797 A1	01/24/2002	S. Yamazaki	
		US 2002/0110941 A1	08/15/2002	S. Yamazaki	
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FOREIGN PATENT DOCUMENTS						
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

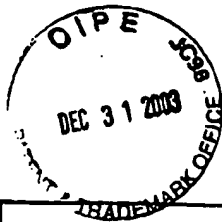
OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
R		S. Kishino, "Novel fundamental of semiconductor device" Ohmesha LTD, 1995, pp. 201-207.	
R		S. Ogura et al., "Design and Characteristic of the Lightly Doped Drain-Source (LDD) Insulated Gate Field-Effect Transistor". IEEE TRANSACTIONS ON ELECTRON DEVICES, Vol. ED-27, No. 8, 1980.	

Examiner Signature	<i>[Signature]</i>	Date Considered	10/20/04
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TL ↓ ↓ ↓ ↓ ↓		US 2001/0030322 A1	10/18/2001	Yamazaki et al.	
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TL		S. Ogura et al., "Design and Characteristic of the Lightly Doped Drain-Source (LDD) Insulated Gate Field-Effect Transistor". IEEE TRANSACTIONS ON ELECTRON DEVICES, Vol. ED-27, No. 8, 1980. P.P. 1359-1367	

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π		U.S. Application Serial No. 10/670,310 filed September 26, 2003, Inventor Akira ISHIKAWA (English Specification with Drawings and Claims)	

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